

CofC



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : SE JEONG PARK ET AL. Confirmation No.: 4403
Serial No. : 09/596,775
Filed : June 19, 2000
TC/A.U. : 2676
Examiner : Hau Nguyen
Patent No. : 6,891,546
Issue Date : May 10, 2005

Docket No. : 00-415
Customer No. : 34704

Certificate
NOV 08 2005
of Correction

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

REQUEST FOR CERTIFICATE OF CORRECTION

Dear Sir:

In accordance with the provisions of 37 CFR 1.322 of the Rules of Practice, the Patent and Trademark Office is respectfully requested to issue a Certificate of Correction in the Patent and Trademark Office as shown in the attached Form PTO 1050 and as set forth below.

Claim 1, line 23, "said components" should be corrected to read --said first and second DRAM banks, said sub-clip loader, and said sub-clip predictor--.

As this mistake was the fault of the Patent Office, it is believed to be in order for the Patent Office to issue a Certificate of Correction and to place such a Certificate of Correction in the file so that such will appear on any of the copies of the patent which are ordered in the future. Moreover,

since this mistake is that of the Patent Office, correction
should be accomplished without charge.

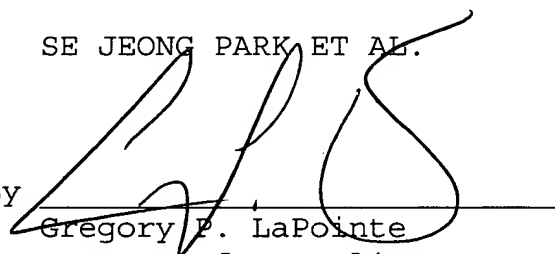
Two (2) copies of Form PTO 1050 embodying the requested
corrections accompany this letter.

If any fees are required in connection with this case, it
is respectfully requested that they be charged to Deposit
Account No. 02-0184.

Respectfully submitted,

SE JEONG PARK ET AL.

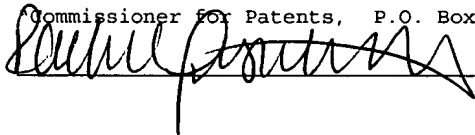
By



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Date: November 1, 2005

I, Rachel Piscitelli, hereby certify that this correspondence is being deposited with the United
States Postal Service with sufficient postage as first class mail in an envelope addressed to:
Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313" on November 1, 2005.



UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,891,546 *B1*
DATED : May 10, 2005
INVENTOR(S) : Se Jeong Park et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 1, line 23, "said components" should be corrected to read --said first and second DRAM banks, said sub-clip loader, and said sub-clip predictor--.

MAILING ADDRESS OF SENDER:

PATENT NO.

6,891,546

No. of additional copies



This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,891,546 *bl*
DATED : May 10, 2005
INVENTOR(S) : Se Jeong Park et al.

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PATENT NO.

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